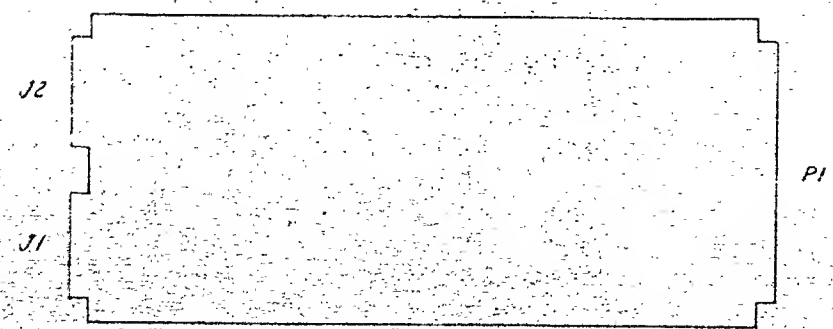


7. LAST REF DESIGNATION USED IC50, R2C15, R2B
6. ALL CAPACITORS ARE .022 MF $\pm 5\%$ 25V.
5. DIRECTIONAL SIGNAL DENOTED BY \rightarrow
4. OUTPUT SIGNAL DENOTED BY \rightarrow
3. INPUT SIGNAL DENOTED BY \rightarrow
2. RESISTORS ARE IN OHMS $\pm 5\%$ 1/4W.
1. FOR DRAWING NO. SEE TABULATION BLOCK.

UNLESS OTHERWISE SPECIFIED



TABULATION BLOCK

DASH NO.	DESCRIPTION	BILL OF MATERIALS	JUMPER
-00	BASIC	73-53507-01	A TO B
-25	LSI 2/25 ROM	73-53507-25	B TO C

ROM CONFIGURATION TABLE

ROM	IC	-00	-25
-01	1	47186	85107
-02	2	47192	85113
-03	6	47187	85108
-04	7	47193	85114
-05	11	47188	85109
-06	12	47194	85115
-07	16	47189	85110
-08	17	47195	85116
-09	21	47266	85111
-10	22	47198	85119
-11	27	47263	85118
-12	31	47267	85112
-13	32	47269	85120
-14	37	47196	85117

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NOTES UNLESS SPECIFIED		CR. 73-53507-XX	
1. TOLERANCES XX = .03 ANGULAR XXX = .010	2. BREAK ALL SHARP EDGES .010 APPROX.	3. ALL DIM. IN INCHES	4. NEXT ASSEMBLY
<p>Computer Automation</p> <p>LOGIC DIAGRAM - PROCESSOR HALF CARD</p>		<p>SIZE DWG. NO.</p> <p>D 75-53507-XX</p>	
<p>DO NOT SCALE DRAWING</p>		<p>SCALE</p> <p>SHT. 1 OF 6</p>	

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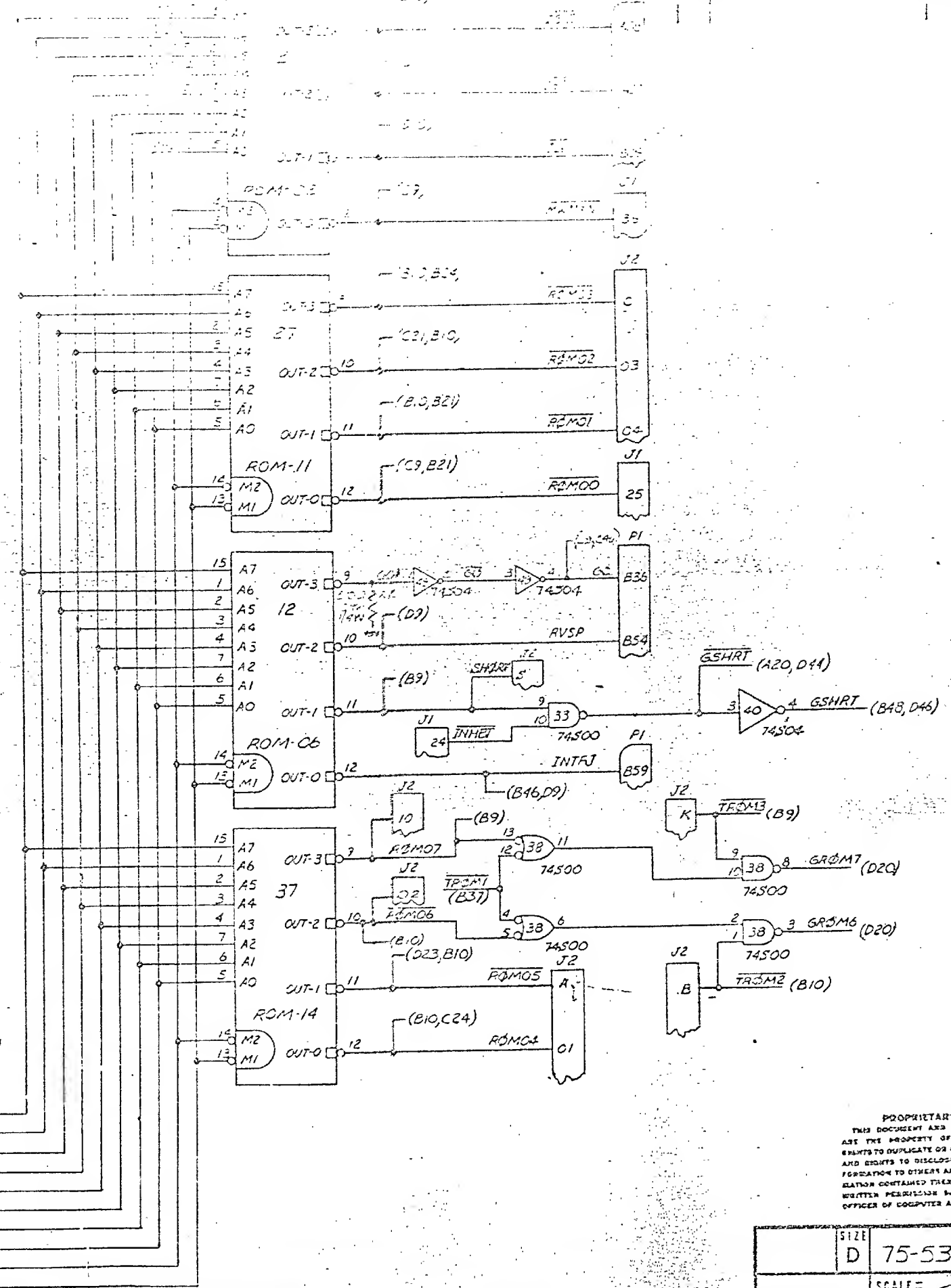
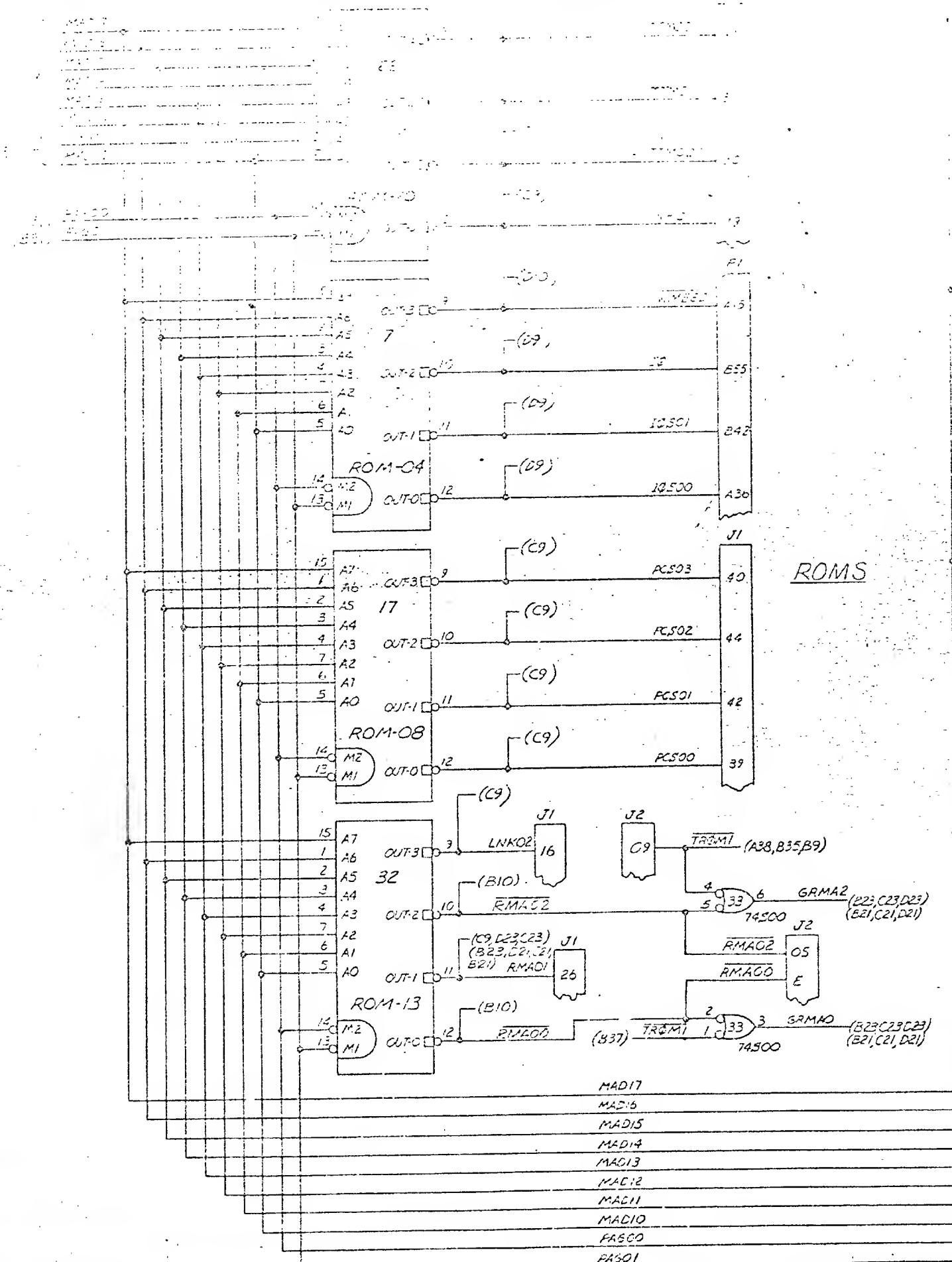
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ALL RESISTORS PACKS ARE 1K, ±5%, 1/8W

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SIZE	D	75-53507-XX	REV	C19
SCALE	1" = 1"		SHEET	2 OF 9



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SIZE	REV.
D	75-53507-XX C19
SCALE	SHEET 5 OF 5

75-53507-XX C19

